



PATENT NO. 5,420,131

U.S. UTILITY Patent Application

101  
110 120 130 140 150 160 170 180 190 200 210 220 230 240 250 260 270 280 290 300 310 320 330 340 350 360 370 380 390 400 410 420 430 440 450 460 470 480 490 500 510 520 530 540 550 560 570 580 590 600 610 620 630 640 650 660 670 680 690 700 710 720 730 740 750 760 770 780 790 800 810 820 830 840 850 860 870 880 890 900 910 920 930 940 950 960 970 980 990 1000

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/825946	F	257 438	687	2811	H. YU
APPLICANTS					
Junji Noguchi Naohumi Ohashi Tatsuyuki Saito					
TITLE					
Semiconductor integrated circuit device and fabrication method for semiconductor integrated circuit device					
PTO-2040 12/89					

ISSUING CHARGE CARD

DISCLAIMER		DRAWING	CHARGE
<input type="checkbox"/> The term of this patent is subject to _____ (Date) has been disclaimed.		<input type="checkbox"/> Sheets Drawg. (Assistant Examiner) _____ (Date) _____	<input type="checkbox"/> Figs. Drawg. (Assistant Examiner) _____ (Date) _____
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____		<input type="checkbox"/> Print Fig. (Primary Examiner) _____ (Date) _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.		NOTICE OF ALLOWANCE ISSUE DATE Amount Due	
(Legal Instruments Examiner) _____ (Date) _____		ISSUE BATCH NO. _____	

WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181, and 182. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A  
(Rev. 8/99)

FILED WITH:  DISK (CRF)  FICHE  CD-ROM  
(Attached to back of next page)

BEST AVAILABLE COPY